

CHIPS (Creating Helpful Incentives to Produce Semiconductors) Act Incentives Interest Form

Please use this editable PDF to help gather information related to your CHIPS Act application and submission as part of our interest form. If there is information you have not yet determined or cannot answer currently, please mark those as N/A in the [interest form](#).

WHAT IS THE CHIPS ACT?

The CHIPS Incentives Program aims to catalyze long-term economically sustainable growth in the domestic semiconductor industry in support of U.S. (United States) economic and national security. BDO USA, LLP welcomes the opportunity to aid your company in compiling and completing a successful application. This Request for Information is designed to gather the data required to complete a Preapplication for CHIPS Act Incentives.

WHERE CAN I FIND RESOURCES TO AID THE SUBMISSION OF A CHIPS ACT APPLICATION?

The CHIPS Program Office released templates, guidebooks, and other resources for the first CHIPS for America funding opportunity for leading-edge, current-generation, mature-node, and back-end semiconductor fabrication facilities. These tools are intended to help applicants collect the information needed to strengthen their proposals. Please reference the link found [here](#) for preapplication and full application details as set forth by the CHIPS program office and the NIST.

COMPANY NAME: _____

COMPANY HEADQUARTERS ADDRESS: _____

CONTACT NAME: _____

CONTACT TITLE: _____

EMAIL ADDRESS: _____

COMPANY WEBSITE: _____

COMPANY REVENUE: _____

COMPANY STRUCTURE (PUBLIC OR PRIVATE): Public Private

MAIN COUNTRIES OF OPERATION: _____

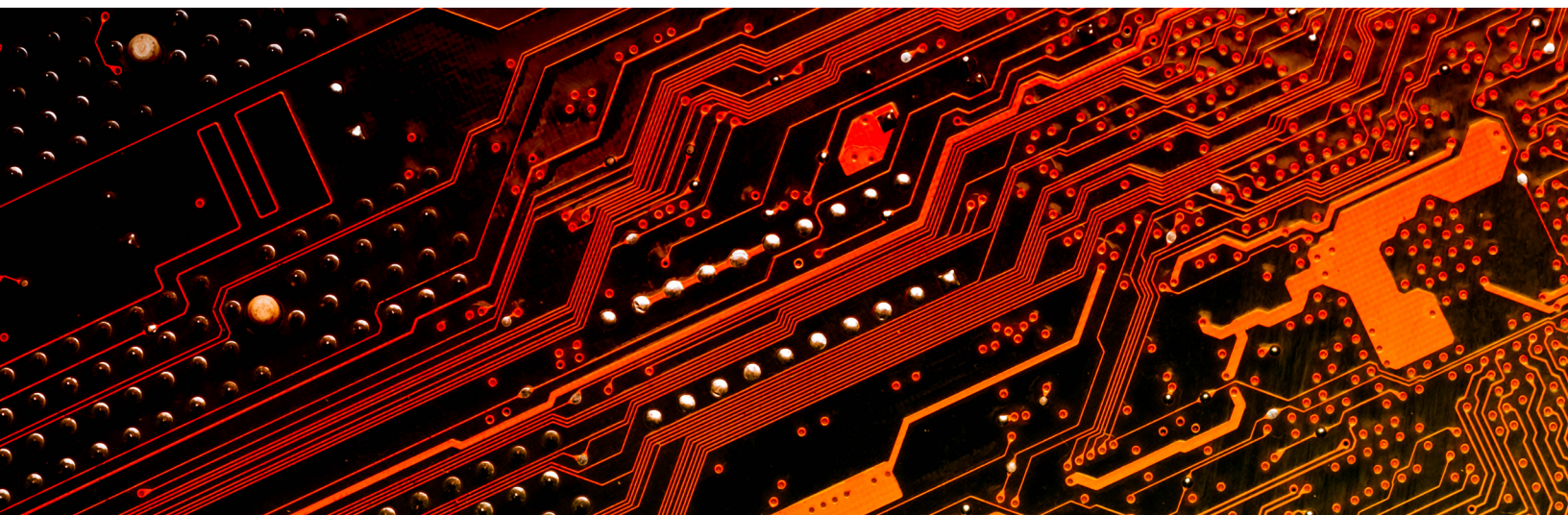
CAN YOUR COMPANY PROVIDE 3 YEARS OF AUDITED FINANCIAL STATEMENTS? Yes No

NUMBER OF CURRENT EMPLOYEES COMPANYWIDE: _____

CAPITAL EXPENDITURES OF PROPOSED PROJECT: _____

NUMBER OF ADDITIONAL JOBS TO BE CREATED BY PROPOSED PROJECT: _____

ESTIMATED WAGE FOR PROPOSED WORKFORCE: _____



Questions

1. Please describe the construction, expansion, or modernization activities proposed for each facility expected to be contained within in the application, including the facility location and existing or required infrastructure.

2. Please indicate the estimated project timeline: 6-12 Months 12-24 Months 24+ Months

3. Please select the type of facility your company plans to build by choosing one or more of the following options.
Select all that apply:

Leading-Edge Facilities that utilize the most advanced front-end fabrication processes for logic, 3D NAND Flash, and Dynamic Random-Access Memory (DRAM) semiconductions

Current-Generation Facilities that produce semiconductors based on 5 nm to 28 nm process technologies and include logic, analog, and mixed-signal devices

Mature-Node Facilities that include logic and analog produced with process technologies above 28 nm, discrete semiconductors, optoelectronics, and sensors

Back-End Production Facilities for the assembly, testing, or packaging of semiconductors that have completed the front-end fabrication process

Semiconductor Materials and Manufacturing Equipment Facilities (included in a future NOFO)

Research and Development Facilities (included in a future NOFO)

Other

4. Please identify your top 3 customers.

5. Please summarize the proposed budget for this project including, estimates of land, building construction/renovation and machinery/equipment (if available).

6. Does your company have the funding on hand at present to undertake this project? Yes No

If you answered 'no' to the question above, please provide a summary of how you plan to raise capital.

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